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Transputer Instruction Set A Compiler Writers Guide

The compiling specification is very similar to a logic program and thus a prototype compiler (and interpreter) may easily be produced in a language such as Prolog. A subset of the occam programming language and the transputer instruction set are used to illustrate the approach.

Transputer Instruction Set: A compiler writer's guide (1988)

1 Introduction. Two instructions exist in the transputer instruction set which are additional to the instruction set published in the "Compiler Writer's Guide", and which are useful to those writing loaders and debuggers. The two instructions concerned are start, and testhardchan. The instructions are described and defined in this document.

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Transputer Instruction Set Appendix

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Transputer Instruction Set A Compiler Writers Guide [EPUB]

The transputer instruction set is based on 8-bit instructions and can easily be used with any word size which is a multiple of 8 bits. The target market for the T100 was to be bus controllers such as Futurebus, and an upgrade for the standard link adapters (C011 etc.).

Transputer - Wikipedia

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Started adding the T801 instructions, from "Transputer Instruction Set - Appendix, Guy Harriman". Started adding the T801/T805 instructions from "Support for debugging/breakpointing in transputers" (INMOS Technical Note 61). Added the -j flag to enable 'j 0' breakpoints. Described current implementation/missing status in the above section.

GitHub - devzendo/transputer-emulator: This is a portable ...

the Transputer instruction set. This interpreter is a small, portable, efficient and extensible run-time. It is intended to be easily ported to handheld computers, mobile phones, and other embedded contexts. In striving for this level of portability, occam programs compiled to Transputer byte-code can currently be run on desktop comput-

The Transterpreter A Transputer Interpreter

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