

Teradyne Catalyst Tester Manual

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Teradyne was focused then, and is today, on reducing single site test times and optimizing parallel test efficiency to lower the cost of test while maintaining compatibility across generations of the platform. The J750 family is the longest running active ATE product which has maintained compatibility throughout its entire lifetime while providing new instrumentation to offer higher ...

J750Ex-HD Family - Teradyne

Teradyne's Memory Test Solutions Teradyne's Magnum memory test solutions are designed with Near DUT Test Technology. The instrument boards are within the test interface unit and test head extension which delivers the shortest distance available, in the industry, between the DUT and the test instrument.

Memory - Teradyne

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1997 - Teradyne introduces Catalyst, the first System-On-A-Chip (SOC) test system. 1998 - Teradyne introduces the Integra J750, a test solution for high volume test of low-cost devices. 2000 - Teradyne Japan Division announces a new generation of image sensor test systems, the IP-750.

Teradyne - Wikipedia

Teradyne Catalyst Tester Manual - Page 9/25 Download File PDF Teradyne Catalyst Tester Manual parentchildbondcom Teradyne's Production Board Test TestStation family of in-circuit test systems share a common software environment and are configurable from a range of a few test points for basic shorts detection up to more than 15,000 test points for more complex PCBAs Page 10/25 Download ...

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